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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number

10/776,054

Filing Date

February 11, 2004

First Named Inventor

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Art Unit

~~Not Yet Assigned~~ 2827

Examiner Name

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NON PATENT LITERATURE DOCUMENTS

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MIN, KYEONG-SIK, KAWAGUCHI, HIROSHI, SAKURAI, TAKAYASU, ZigZag Super Cut-off CMOS (ZSCCMOS) Block Activation with Self-Adaptive Voltage Level Controller: An Alternative to Clock-Gating Scheme in Leakage Dominant Era, 2003 IEEE International Solid-State Circuits Conference, February 12, 2003, Salon 1-6, pp. 400-401 and 501-502.

**EXAMINER
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